X-1455 US PATENT

METHOD AND SYSTEM FOR A MULTI-CHANNEL ADD-COMPARE-SELECT UNIT

ABSTRACT

A circuit for a multi-channel add-compare-select unit is disclosed. The circuit includes a compare unit and a datapath. The datapath is coupled to the compare unit, and includes a number of adder units, a selection unit (which is coupled to the adder units), and a number of clocked storage stages.